

SEMITRANS® 3

IGBT4 Modules

SKM600GM12E4

Features*

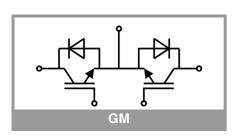
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532

Typical Applications

- Matrix Inverter
- · Bidirectional switch

Remarks

· Case temperature limited to $T_c = 125$ °C max, recomm. $T_{op} = -40 ... +150$ °C, product rel. results valid for $T_i = 150$ °C



Absolute	Maximum Ratir	ngs		
Symbol	Conditions		Values	Unit
IGBT	•			' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '
V _{CES}	T _j = 25 °C		1200	V
T _j =	T _i = 175 °C	T _c = 25 °C	860	Α
	11 _j = 175 C	T _c = 80 °C	702	Α
I _{Cnom}			600	А
I _{CRM}			1800	А
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	iode			
V_{RRM}	T _j = 25 °C		1200	V
l _F	T _j = 175 °C	T _c = 25 °C	623	А
		T _c = 80 °C	466	А
I _{Fnom}			500	А
I _{FRM}			1200	А
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 ^{\circ}\text{C}$		2736	А
Tj			-40 175	°C
Module				
I _{t(RMS)}			500	А
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	٧

Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
$\begin{array}{c} V_{CE(sat)} \\ \hline V_{GE} = 15 \text{ V} \\ \text{chiplevel} \end{array}$		T _j = 25 °C		1.80	2.05	V
		T _j = 150 °C		2.20	2.42	V
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V
		T _j = 150 °C		0.70	0.80	V
	V _{GE} = 15 V	T _j = 25 °C		1.67	1.92	mΩ
	chiplevel	T _j = 150 °C		2.5	2.7	mΩ
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_C=24$ m	A	5	5.8	6.5	V
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			5	mA
C _{ies}	V 05.V	f = 1 MHz		37.2		nF
C _{oes}	$V_{CE} = 25 \text{ V}$ $V_{GE} = 0 \text{ V}$	f = 1 MHz		2.32		nF
C _{res}		f = 1 MHz		2.04		nF
Q _G	V _{GE} = - 8 V+ 15 V			3400		nC
R _{Gint}	T _j = 25 °C			1.3		Ω
t _{d(on)}	$\begin{array}{l} I_{C} = 600 \text{ A} \\ V_{GE} = +15/\text{-}15 \text{ V} \\ R_{G \text{ on}} = 1.8 \ \Omega \\ R_{G \text{ off}} = 1 \ \Omega \\ \text{di/dt}_{on} = 9100 \text{ A/}\mu\text{s} \\ \text{di/dt}_{off} = 4000 \text{ A/}\mu\text{s} \end{array}$	T _j = 150 °C		156		ns
t _r		T _j = 150 °C		68		ns
E _{on}		T _j = 150 °C		30		mJ
t _{d(off)}		T _j = 150 °C		522		ns
tf		T _j = 150 °C		138		ns
E _{off}		T _j = 150 °C		77		mJ
R _{th(j-c)}	per IGBT				0.049	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0.	.81 W/(m*K))		0.032		K/W
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.016		K/W



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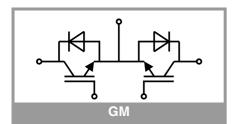
Matrix Inverter

· Bidirectional switch

Remarks

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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					
$V_F = V_{EC}$	$V_F = V_{EC}$ $V_{GE} = 0 V$ chiplevel	T _j = 25 °C		2.28	2.63	V
		T _j = 150 °C		2.28	2.61	V
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chiplevel	T _j = 25 °C		1.64	1.88	$m\Omega$
		T _j = 150 °C		2.3	2.5	mΩ
I_{RRM}	$I_F = 600 \text{ A}$ $di/dt_{off} = 8500 \text{ A/}\mu\text{s}$ $V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T _j = 150 °C		559		Α
Q_{rr}		T _j = 150 °C		98		μC
E_{rr}		T _j = 150 °C		39		mJ
R _{th(j-c)}	per diode			0.095	K/W	
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.039		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.028		K/W
Module						
L _{CE}				15		nH
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ
	switch	T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling			0.00879		K/W
$R_{\text{th(c-s)2}}$	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/(m*K))			0.014		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.008		K/W
Ms	to heat sink M6		3		5	Nm
M_{t}		to terminals M6	2.5		5	Nm
						Nm
W					325	g



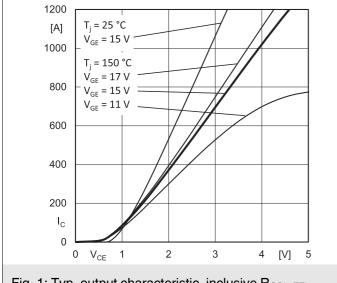


Fig. 1: Typ. output characteristic, inclusive R_{CC'+ EE'}

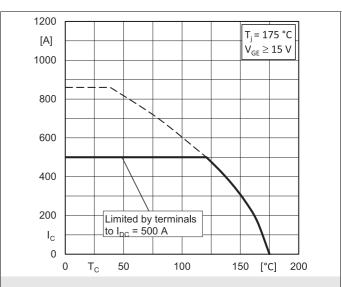


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

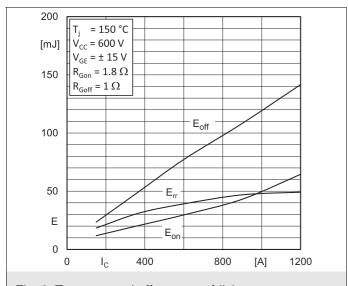


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

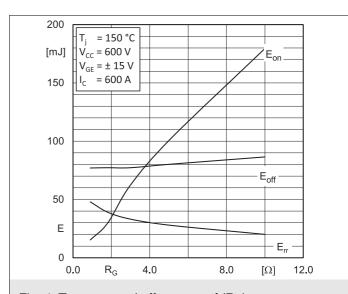
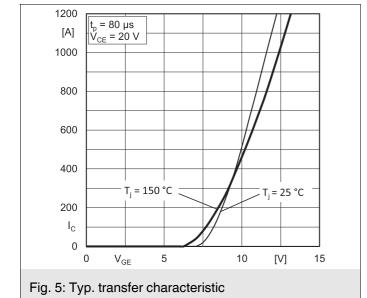


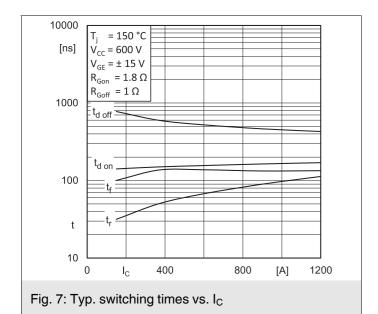
Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

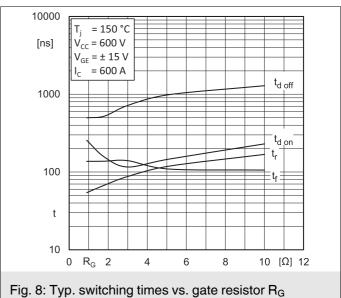
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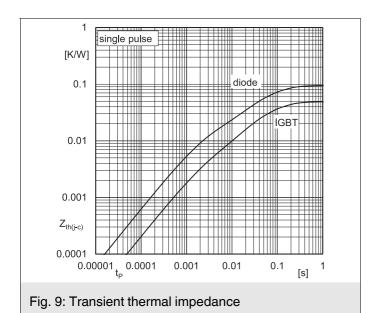
[V]

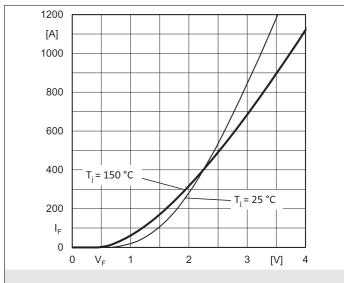


 $I_{\text{Cpuls}} = 600 \text{ A}$ $V_{\text{cc}} = 600 \text{ V}$ 16 12 8 4 0 -4 V_{GE} 1000 Q_G 2000 3000 4000 [nC] Fig. 6: Typ. gate charge characteristic









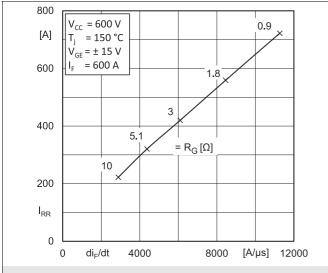


Fig. 10: Typ. CAL diode forward charact., incl. R_{CC'+ EE'}



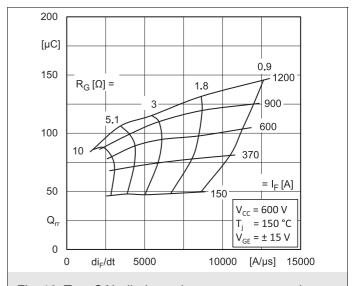
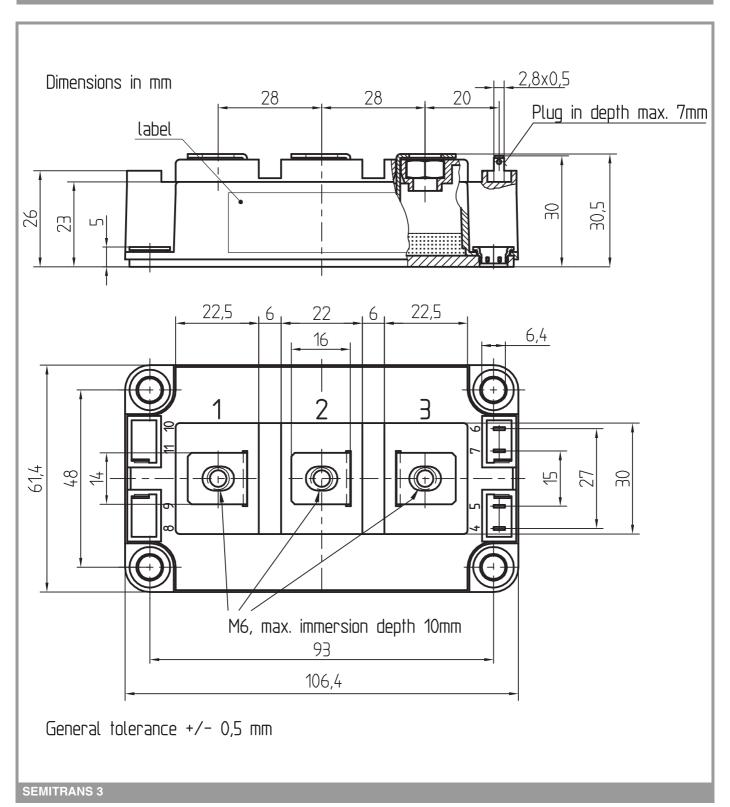
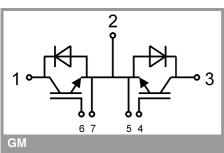


Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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