

# 6MBI300V-170-50

**IGBT Modules** 

# **IGBT MODULE (V series)** 1700V / 300A / 6 in one package

#### **■** Features

Compact Package P.C.Board Mount Low VcE (sat)

#### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as welding machines



#### ■ Maximum Ratings and Characteristics

# ● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)

Items		Symbols	Conditions	Conditions		Units		
Inverter	Collector-Emitter voltage		Vces				V	
	Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
	Collector current		Ic	Continuous	Tc=25°C	450		
				Continuous	Tc=100°C	300		
			I <sub>C pulse</sub>	1ms	1ms		Α	
			-lc					
			-I <sub>C pulse</sub>	1ms	1ms			
	Collector power dissipation		Pc	1 device	1 device		W	
Ju	Junction temperature		Ti					
Operating junction temperature (under switching conditions)		Тјор			150	°C		
Case temperature		Tc			125			
Storage temperature		T <sub>stg</sub>			-40 ~ 125			
las	olation voltage	Between terminal and copper base (*1)	Viso	AC: 1min		3400	VAC	
ISC		Between thermistor and others (*2)		AC : 1min.	AC . IIIIII.			
0-	crew torque	Mounting (*3)	-			3.5	N m	
30		Terminals (*4)	-			4.5	IN III	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable Value : 2.5-3.5 Nm (M5)

Note \*4: Recommendable Value : 3.5-4.5 Nm (M6)

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# ● Electrical characteristics (at T<sub>i</sub>= 25°C unless otherwise specified)

Items		Cumbala	Conditions		Characteristics			Units
		Symbols			min.	typ.	max.	Units
	Zero gate voltage collector current	te voltage collector current Ices VGE = 0V, VCE = 1700V		-	-	3.0	mA	
Inverter	Gate-Emitter leakage current	I <sub>GES</sub>	$V_{CE} = 0V$ , $V_{GE} = \pm 20V$		-	-	600	nA
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 300mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	.,	V <sub>GE</sub> = 15V I <sub>C</sub> = 300A	T <sub>i</sub> =25°C	-	2.45	2.90	V
		(terminal)		T <sub>j</sub> =125°C	-	2.90	-	
		(terrilinal)		T <sub>j</sub> =150°C	-	2.95	-	
			V <sub>GE</sub> = 15V I <sub>C</sub> = 300A	T <sub>i</sub> =25°C	-	2.00	2.45	
		V <sub>CE (sat)</sub> (chip)		T <sub>j</sub> =125°C	-	2.45	-	
		(Criip)		T <sub>j</sub> =150°C	-	2.50	-	
	Internal gate resistance	R <sub>G (int)</sub>	-		-	2.50	-	Ω
	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	30	-	nF
	Turn-on time	ton	V 000V	-	900	-	nsec	
		t	V <sub>cc</sub> = 900V I <sub>c</sub> = 300A	-	400	-		
		t <sub>r (i)</sub>	V <sub>GE</sub> = ±15V	-	100	-		
		toff	R <sub>G</sub> = 4.7Ω L <sub>S</sub> = 80nH		-	1300		-
	Turn-off time	tr			-	100		-
	Forward on voltage	V <sub>F</sub> (terminal)	V <sub>GE</sub> = 0V, I <sub>F</sub> = 300A	T <sub>j</sub> =25°C	-	2.25	2.70	
				T <sub>j</sub> =125°C	-	2.55	-	
				T <sub>j</sub> =150°C	-	2.55	-	
			V <sub>GE</sub> = 0V, I <sub>F</sub> = 300A	T <sub>j</sub> =25°C	-	1.80	2.25	V
		V <sub>F</sub>		T <sub>j</sub> =125°C	-	2.10	-	1
		(chip)		T <sub>j</sub> =150°C	-	2.10	-	
	Reverse recovery time	trr	I <sub>F</sub> = 300A		-	250	-	nsec
ō			T = 25°C		-	5000	-	Ω
Thermistor	Resistance	R	T = 100°C		465	495	520	
The	B value	В	T = 25 / 50°C		3305	3375	3450	K

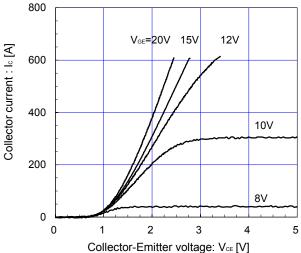
#### ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items			min.	typ.	max.	Units
Thermal registance (1device)	R <sub>th(j-c)</sub>	Inverter IGBT	-	-	0.090	°C/W
Thermal resistance (1device)		Inverter FWD	-	-	0.150	
Contact thermal resistance (1device) (*5)	R <sub>th(c-f)</sub>	with Thermal Compound	-	0.0167	-	

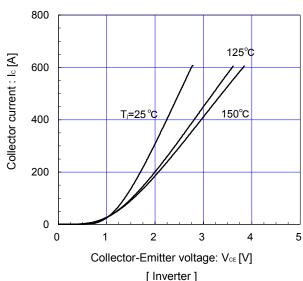
Note \*5: This is the value which is defined mounting on the additional cooling fin with thermal compound.

### **■** Characteristics (Representative)

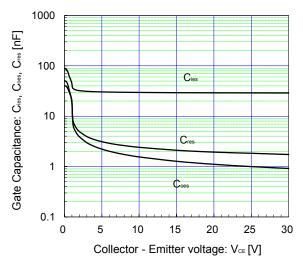
[ Inverter ] Collector current vs. Collector-Emitter voltage (typ.)  $T_{i}{=}~25^{\circ}\text{C}~/~\text{chip}$ 



[ Inverter ] Collector current vs. Collector-Emitter voltage (typ.)  $V_{\text{GE}}$ =15V / chip

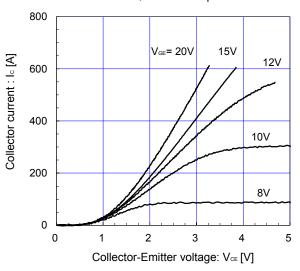


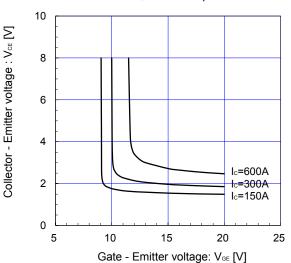
Gate Capacitance vs. Collector-Emitter voltage (typ.)  $V_{\text{GE}}$ =0V, f= 1MHz,  $T_{\text{J}}$ = 25°C



[ Inverter ]

Collector current vs. Collector-Emitter voltage (typ.)  $T_i = 150^{\circ}C / chip$ 

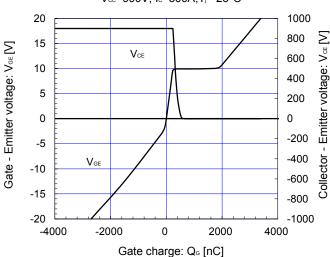




[ Inverter ]

Dynamic gate charge (typ.)

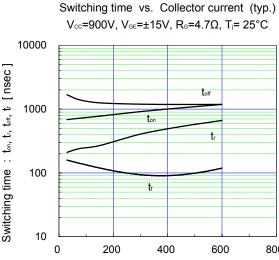
Vcc=900V, Ic=300A,T<sub>i</sub>= 25°C



600

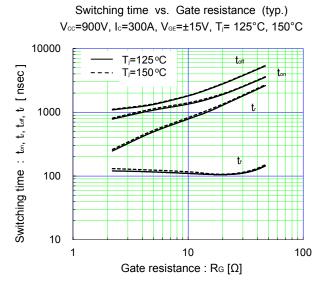
800

[Inverter]



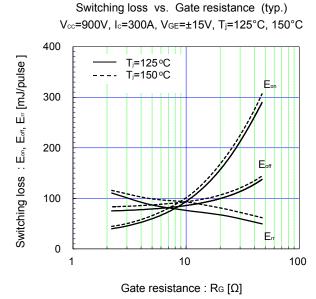
[Inverter]

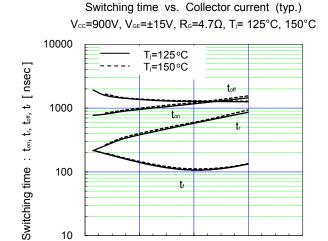
Switching time : ton, tr, toff, tr [ nsec ] 800 Collector current: Ic [A]



[Inverter]

[Inverter]





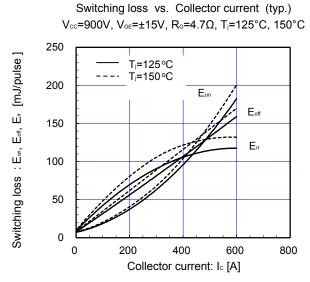
400

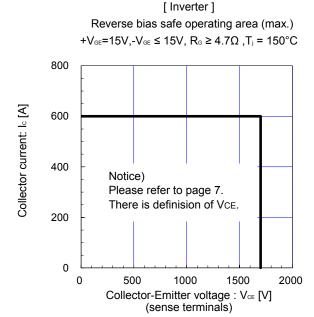
Collector current: Ic [A]

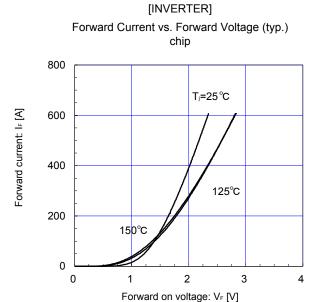
[Inverter]

200

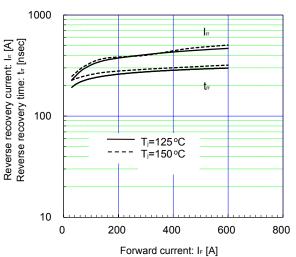
0



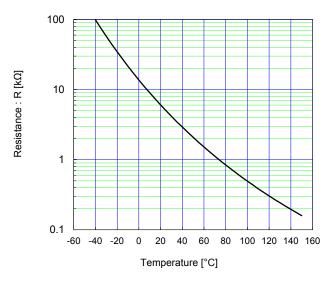




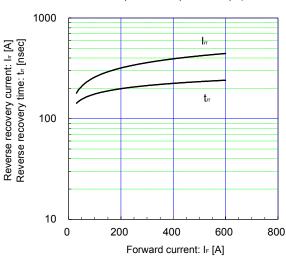
[INVERTER] Reverse Recovery Characteristics (typ.)  $V_{\text{cc}} = 900 \text{V}, \ V_{\text{ce}} = \pm 15 \text{V}, \ R_{\text{e}} = 4.7 \Omega, \ T_{\text{j}} = 125^{\circ} \text{C}, \ 150^{\circ} \text{C}$ 



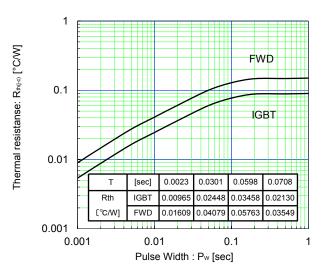
[THERMISTOR]
Temperature characteristic (typ.)



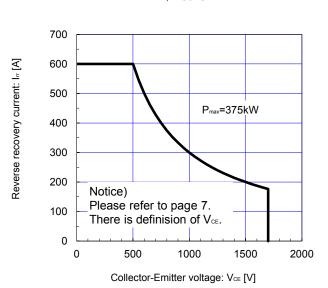
 $[INVERTER] $$ Reverse Recovery Characteristics (typ.) $$ V_{cc}=900V, V_{eE}=\pm15V, R_{c}=4.7\Omega, T_{j}=25^{\circ}C $$$ 



Transient Thermal Resistance (max.)



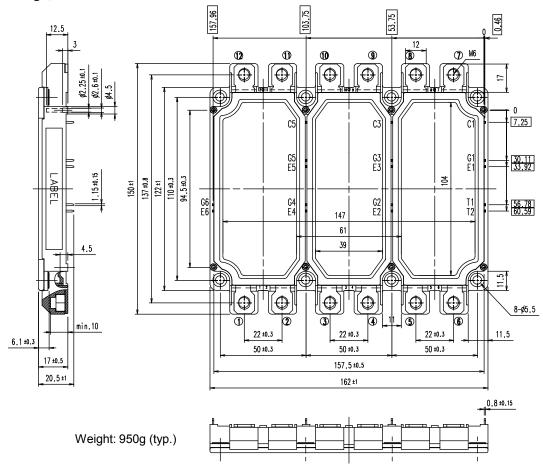
FWD safe operating area (max.)
T<sub>i</sub>=150°C



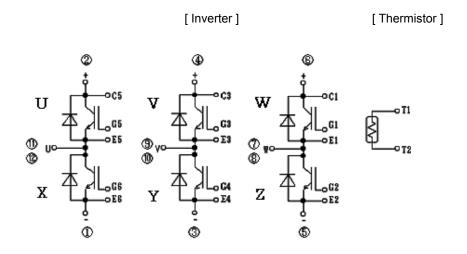
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# **■** Outline Drawings, mm

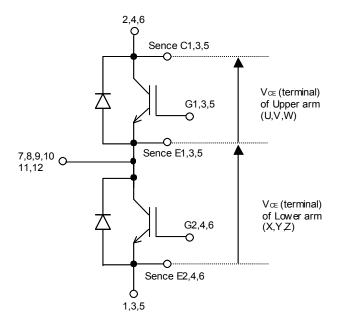


# **■** Equivalent Circuit



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#### ■ Definition of switching characteristics



Switching characteristics of VCE is defined between Sense C1,3,5 and Sense E1,3,5 for Upper arm(U,V,W) and Sense E1,3,5 and Sense E2,4,6 for Lower arm(X,Y,Z).

Please use these terminals whenever measure spike voltage.

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   Industrial reports at a

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