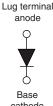


# **High Performance Schottky Rectifier, 120 A**





HALF-PAK (D-67)

Lug terrinite
anode
1
Y
6
Base
cathode

PRODUCT SUMMARY			
I <sub>F(AV)</sub>	120 A		
$V_{R}$	45 V		
Package	HALF-PAK (D-67)		
Circuit	Single diode		

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- · Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION**

The VS-120NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U				
I <sub>F(AV)</sub>	Rectangular waveform	120	Α			
V <sub>RRM</sub>		45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	26 000	Α			
V <sub>F</sub>	120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.62	V			
T <sub>J</sub>	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-120NQ045PbF	UNITS		
Maximum DC reverse voltage	$V_{R}$	45	V		
Maximum working peak reverse voltage	$V_{RWM}$	45	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 105 °C, rectangular waveform		120	
Maximum peak one cycle non-repetitive surge current	lee.	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	26 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	1550	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 13 A, L = 1 mH		81	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \text{ x } V_R$ typical		13	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VA		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	120 A	T. <sub>1</sub> = 25 °C	0.63	V
Maximum forward voltage drop		240 A	1J=25 C	0.86	
See fig. 1	V FM (1)	120 A	T <sub>J</sub> = 125 °C	0.62	
		240 A		0.81	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mΛ
See fig. 2		T <sub>J</sub> = 125 °C		500	mA
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		10 000	V/µs

#### Note

 $<sup>^{(1)}</sup>$  Pulse width  $< 500 \mu s$ 

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temper	erature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junctic	n thermal resistance, junction to case R <sub>thJC</sub> DC operation See fig. 4		0.38	°C/W		
Typical thermal resistance, case to h	eatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	0.05	
Approximate weight				30	g	
				1.06	oz.	
Mounting torque minimum maximum			Non-lubricated threads	3 (26.5)		
				4 (35.4)	N⋅m	
Tamainal tamana	minimum		Non-lubricateu tilleaus	3.4 (30)	(lbf · in)	
Terminal torque	maximum			5 (44.2)		
Case style				HALF-PAR	module	

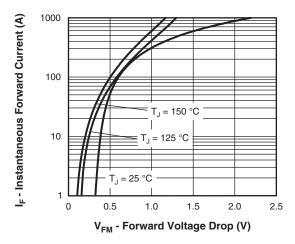


Fig. 1 - Maximum Forward Voltage Drop Characteristics

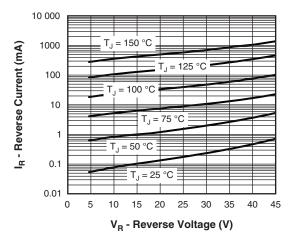


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



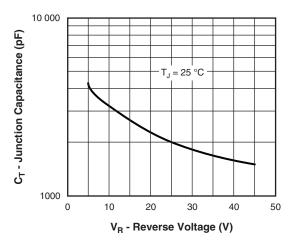


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

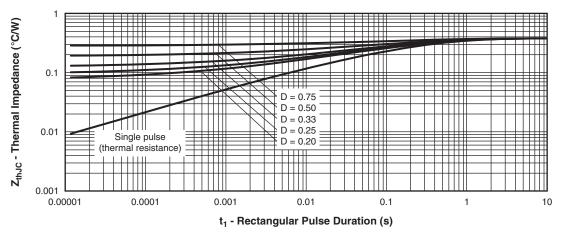


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

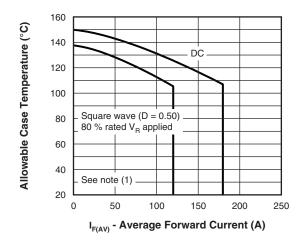


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

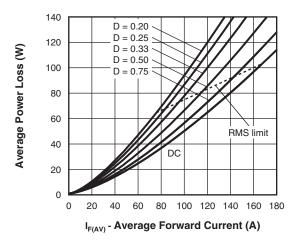
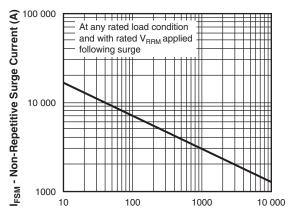


Fig. 6 - Forward Power Loss Characteristics



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

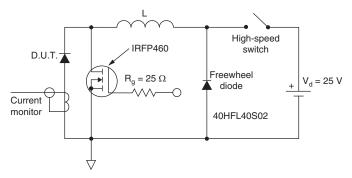


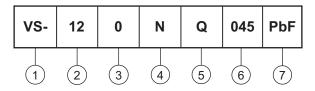
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

### **ORDERING INFORMATION TABLE**

### Device code



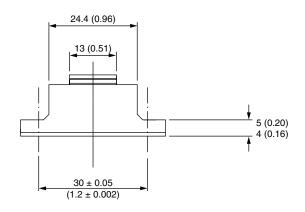
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- Product silicon identification
- 4 N = Not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (045 = 45 V)
- 7 Lead (Pb)-free

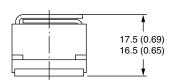
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

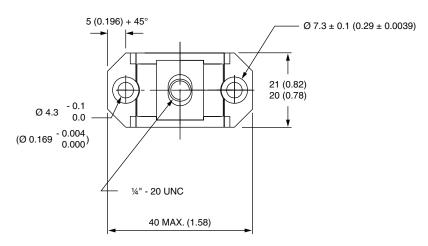


### **D-67 HALF-PAK**

### **DIMENSIONS** in millimeters (inches)









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Vishay

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