

SK80MB120CR03TE1



SEMITOP®E1

Half-Bridge (Full SiC)

Engineering Sample SK80MB120CR03TE1

Target Data

Features*

- Optimized design for superior thermal performance
- Extremely low inductance design
- Press-Fit contact technology
- 1200V Planar Gen3 SiC MOS
- Simple to drive with +15V gate voltage
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Switched Mode Power Supplies
- Energy Storage Systems
- Electric Vehicle charging
- UPS
- Solar
- Motor Drives

Remarks

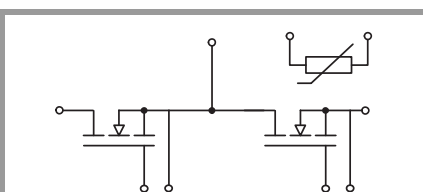
- Recommended $T_{j,op} = -40 \dots +150 \text{ °C}$
- Recommended turn-off / turn-on gate voltage $V_{GS} = -4 \dots 0 / +15V$

Footnotes

1) SEMIKRON Exclusive High Performance Thermal Paste (HPTP), available as pre-applied

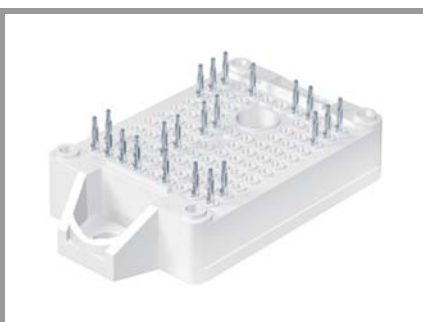
Absolute Maximum Ratings				
Symbol	Conditions	Values	Unit	
MOSFET 1				
V_{DSS}		1200	V	
I_D	$T_j = 175 \text{ °C}$	$T_s = 25 \text{ °C}$	98	A
		$T_s = 70 \text{ °C}$	82	A
I_{DM}	Pulse width t_p limited by T_{jmax}	240	A	
$I_{DM,repitive}$		120	A	
V_{GS}	Max. transient gate - source voltage	-8 ... 19	V	
T_j		-55 ... 175	°C	
Integrated body diode				
I_{FM}	Pulse width t_p limited by T_{jmax}	240	A	
$I_{FM,repitive}$		120	A	

Absolute Maximum Ratings			
Symbol	Conditions	Values	Unit
Module			
$I_{t(RMS)}$	$\Delta T_{terminal}$ at PCB joint = 30 K, per pin	30	A
T_{stg}	module without TIM	-40 ... 125	°C
V_{isol}	AC, sinusoidal, $t = 1 \text{ min}$	2500	V



MB-T

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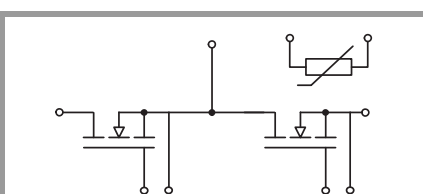
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Footnotes

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MB-T

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
MOSFET 1					
$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 0.1 \text{ mA}, T_j = 25 \text{ °C}$	1200			V
$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 23 \text{ mA}, T_j = 25 \text{ °C}$	1.8	2.5	3.6	V
I_{DSS}	$V_{GS} = 0 \text{ V}, V_{DS} = 1200 \text{ V}, T_j = 25 \text{ °C}$			1	mA
I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 15 \text{ V}, T_j = 25 \text{ °C}$			200	nA
$R_{DS(on)}$	$V_{GS} = 15 \text{ V}$ $I_D = 83 \text{ A}$ chipelevel	$T_j = 25 \text{ °C}$	16	22	mΩ
		$T_j = 150 \text{ °C}$	25		mΩ
C_{iss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		6800		pF
C_{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		260		pF
C_{riss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		20		pF
R_{Gint}	$T_j = 25 \text{ °C}$		5.9		Ω
Q_G	$V_{DD} = 800 \text{ V}, V_{GS} = -4 \text{ V} \dots 15 \text{ V},$ $I_D = 83 \text{ A}$		236		nC
$t_{d(on)}$	$V_{DD} = 600 \text{ V}$ $V_{GS} = 15/-4 \text{ V}$	$T_j = 150 \text{ °C}$	36		ns
$t_{d(off)}$	$I_D = 80 \text{ A}$	$T_j = 150 \text{ °C}$	90		ns
t_r	$R_{Gon} = 1 \text{ Ω}$	$T_j = 150 \text{ °C}$	16		ns
t_f	$R_{Goff} = 1 \text{ Ω}$	$T_j = 150 \text{ °C}$	23		ns
E_{on}	$di/dt_{off} = 5.6 \text{ kA/μs}$	$T_j = 150 \text{ °C}$	1.02		mJ
E_{off}	$di/dt_{on} = 7.1 \text{ kA/μs}$ $dv/dt = 28 \text{ kV/μs}$	$T_j = 150 \text{ °C}$	0.69		mJ
$R_{th(j-s)}$	per MOSFET, $\lambda_{paste} = 2.5 \text{ W/(mK)}^1$		0.51		K/W
Integrated body diode					
$V_F = V_{SD}$	$-I_D = 41 \text{ A}$ $V_{GS} = -4 \text{ V}$ chipelevel	$T_j = 25 \text{ °C}$	4.58		V
		$T_j = 150 \text{ °C}$	4.30		V
$V_{F0} = V_{SD0}$	chipelevel	$T_j = 25 \text{ °C}$	3.80		V
		$T_j = 150 \text{ °C}$	3.60		V
$r_F = r_{SD}$	chipelevel	$T_j = 25 \text{ °C}$	19		mΩ
		$T_j = 150 \text{ °C}$	17		mΩ
t_{rr}	$V_{DD} = 600 \text{ V}$	$T_j = 150 \text{ °C}$	33		ns
Q_{rr}	$-I_D = 80 \text{ A}$	$T_j = 150 \text{ °C}$	1.4		μC
I_{rr}	$V_{GS} = -4 \text{ V}$	$T_j = 150 \text{ °C}$	86		A
E_{rr}	$R_{Gon} = 1 \text{ Ω}$ $di/dt_{off} = 7.3 \text{ kA/μs}$	$T_j = 150 \text{ °C}$	0.81		mJ

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Module					
L_{CE}			9		nH
M_s	to heatsink	1.6		2.3	Nm
w	weight		25		g

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Temperature Sensor					
R_{100}	$T_r = 100 \text{ °C}$		$493 \pm 5\%$		Ω
$B_{100/125}$	$R(T) = R_{100} \exp[B_{100/125}(1/T - 1/T_{100})]$; $T[K]$;		$3550 \pm 2\%$		K

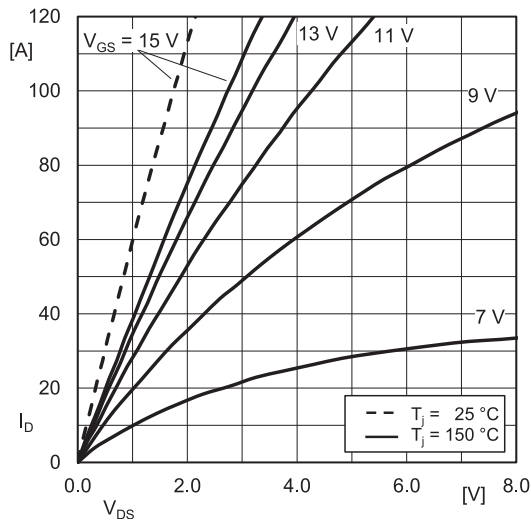


Fig. 1: Typ. MOSFET forward output characteristic, incl. $R_{DD'+SS'}$

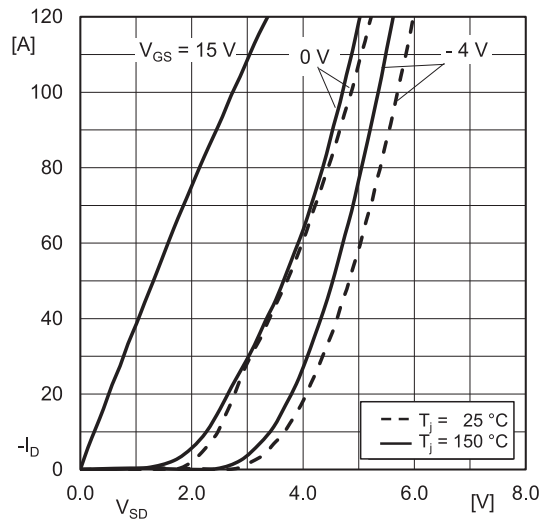


Fig. 1a: Typ. MOSFET reverse output characteristics, incl. $R_{DD'+SS'}$

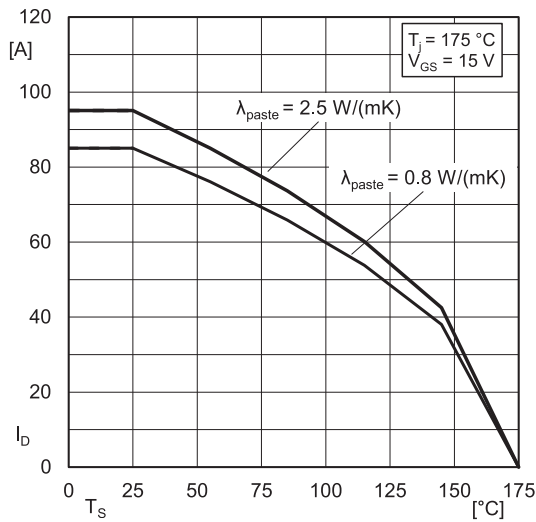


Fig. 2: Rated current vs. temperature $I_D = f(T_S)$

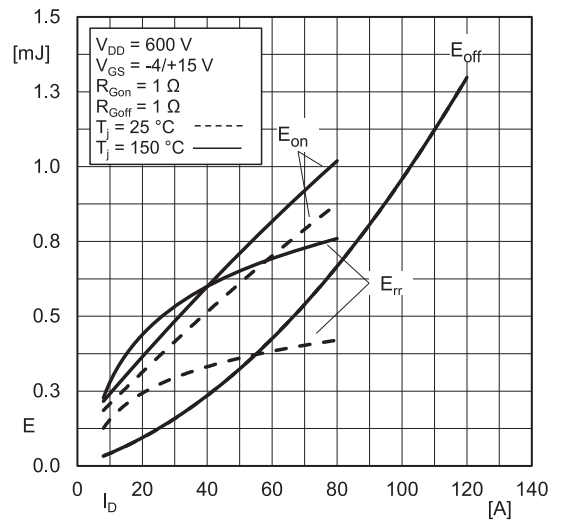


Fig. 3: Typ. switching energy $E = f(I_D)$ at R_{G1}

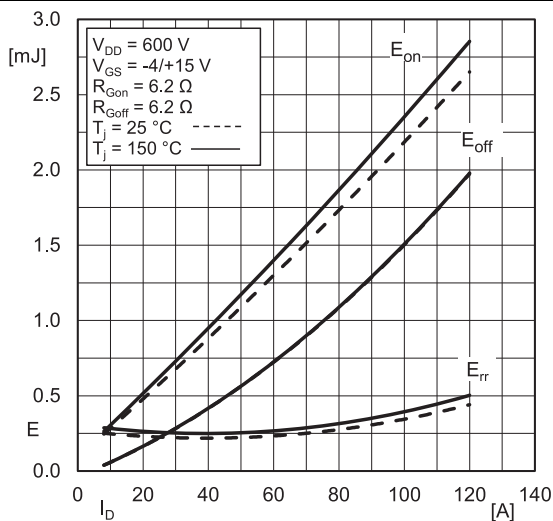


Fig. 3a: Typ. switching energy $E = f(I_D)$ at R_{G2}

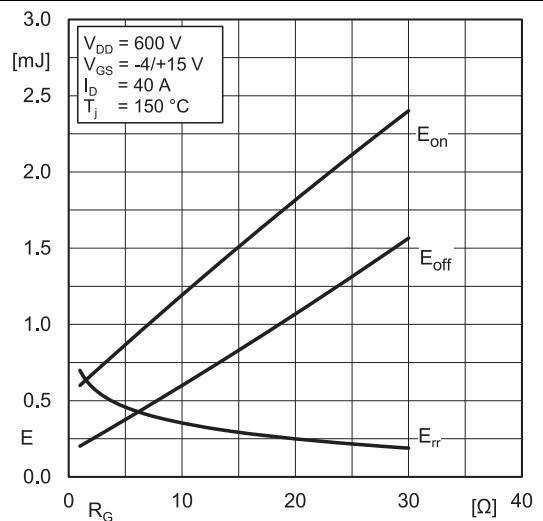


Fig. 4: Typ. switching energy $E = f(R_G)$ at I_{D1}

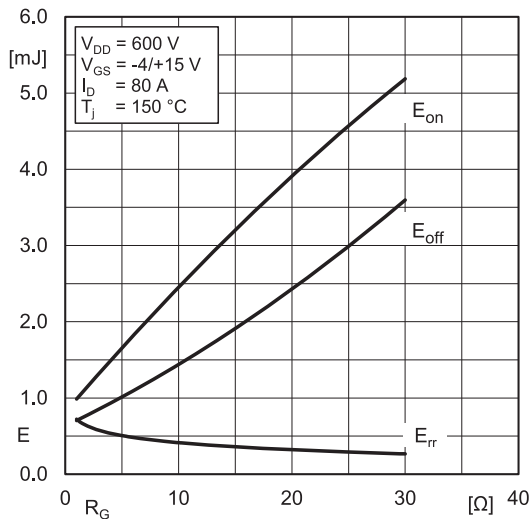


Fig. 4a: Typ. switching energy $E = f(R_G)$ at I_{D2}

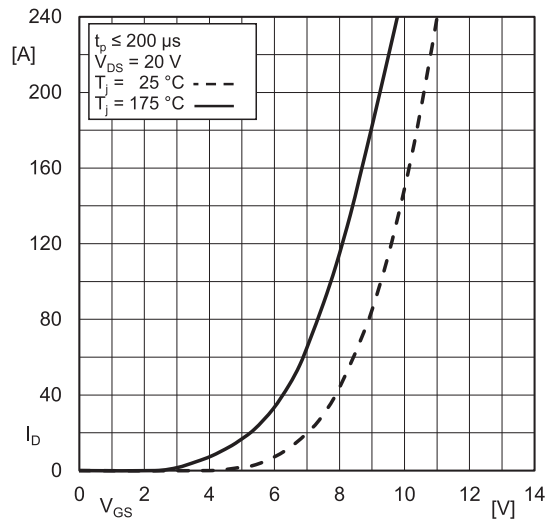


Fig. 5: Typ. MOSFET transfer characteristic

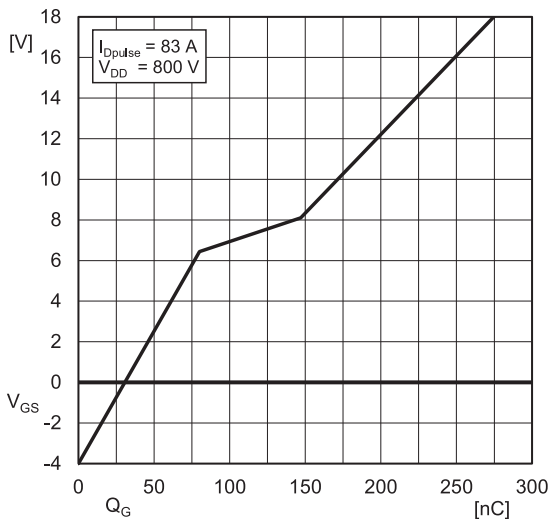


Fig. 6: Typ. MOSFET gate charge characteristic

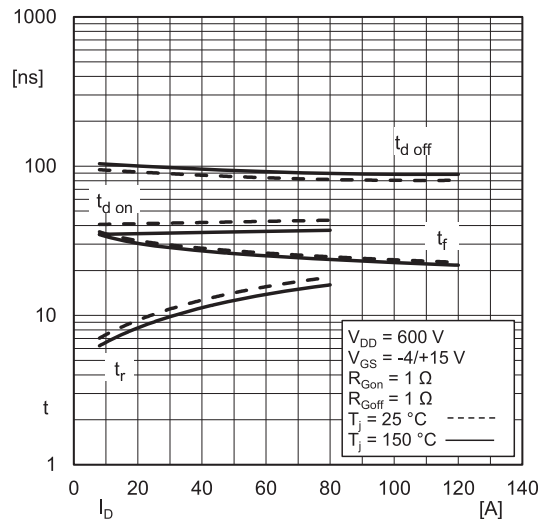


Fig. 7: Typ. switching times $t = f(I_D)$ at R_{G1}

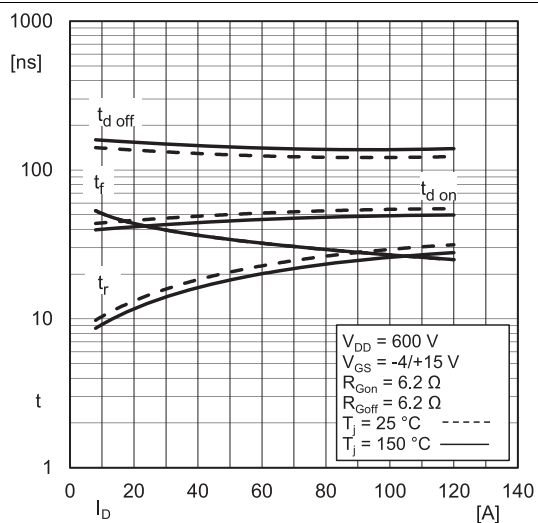


Fig. 7a: Typ. switching times $t = f(I_D)$ at R_{G2}

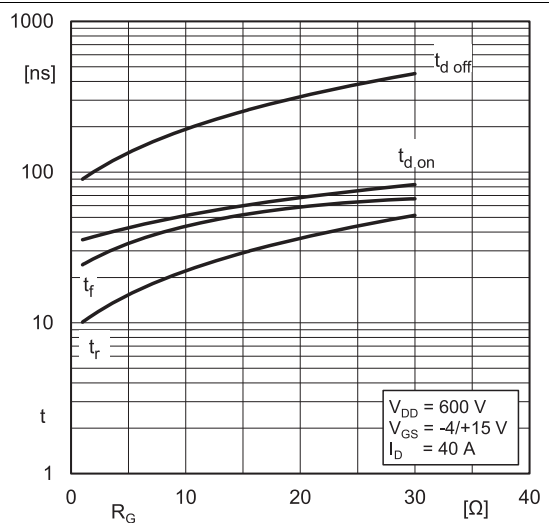
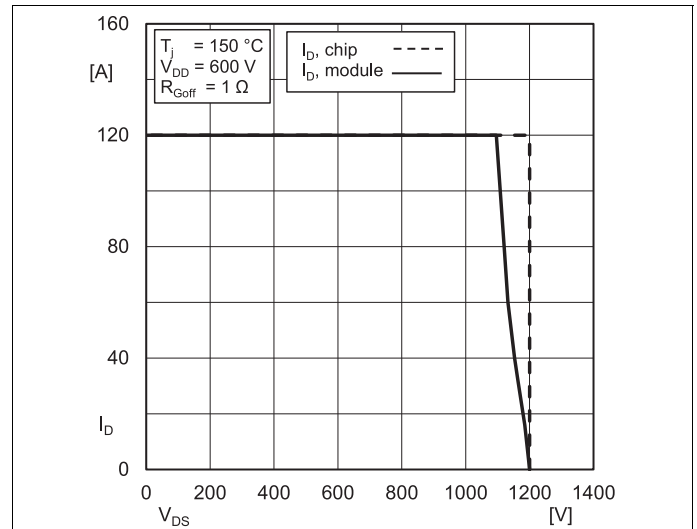
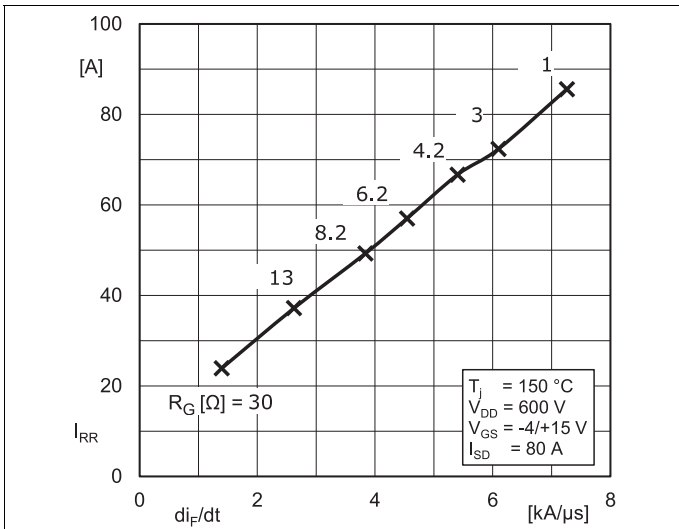
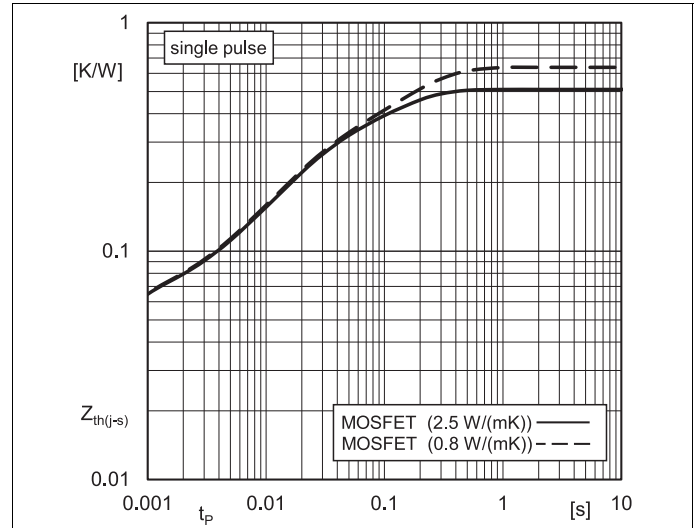
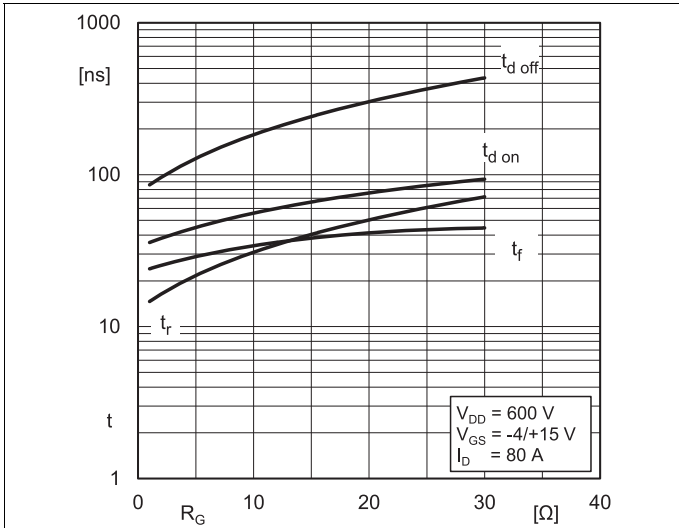
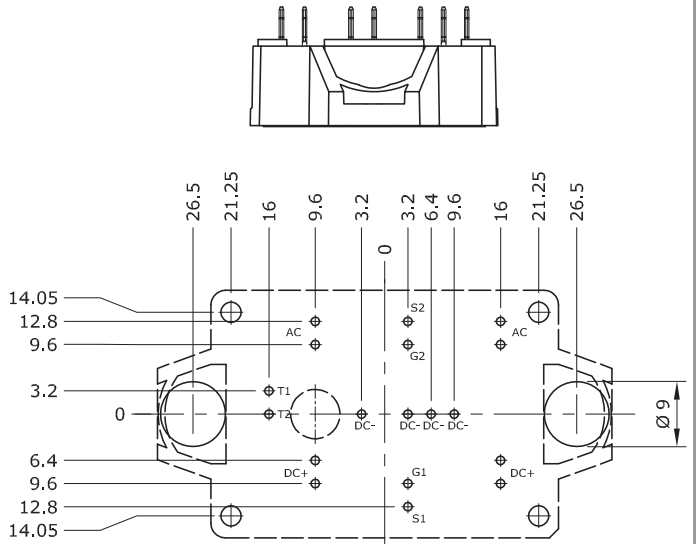
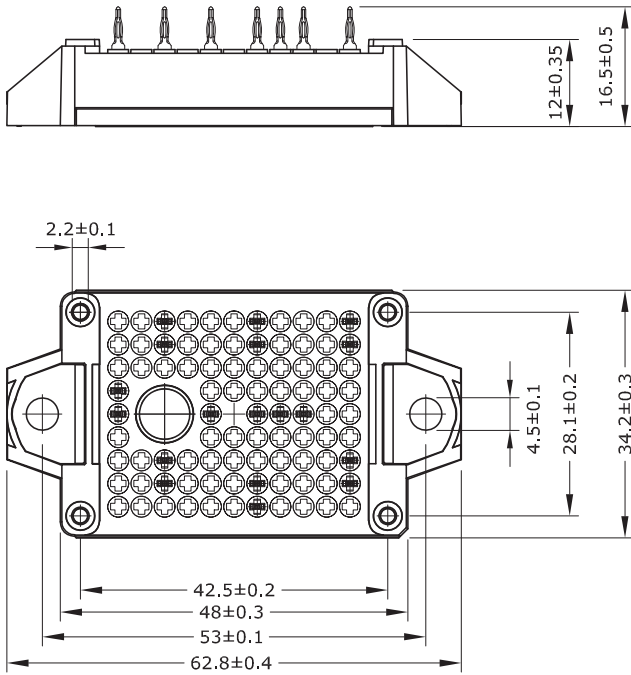


Fig. 8: Typ. switching times $t = f(R_G)$ at I_{D1}

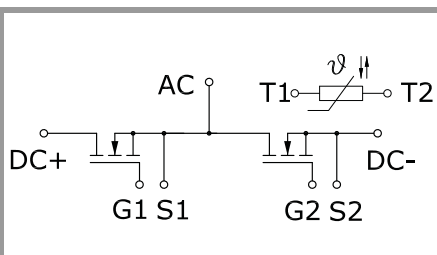


SK80MB120CR03TE1



- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern $\boxed{\phi \ 0.1}$
- Diameters of drill $\phi \ 1.15\text{mm}$
- Copper thickness in hole 25 - 50 μm
- Hole specification for contacts:
refer to SEMITOP E1/E2 Mounting Instruction

SEMITOP®E1



MB-T

This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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