

SEMiX[®] 5

Trench IGBT Modules

SEMiX205GD12E4

Features*

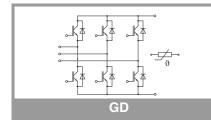
- Solderless assembly solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and robust internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

Typical Applications

- AC inverter drives
- UPS
- Electronic Welding

Remarks

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- Please refer to Semix5p Technical Explanations for mounting conditions
- For storage and case temperature with TIM see document TP(HALA P8) SEMiX5p



Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _i = 25 °C		1200	V
I _C		T _c = 25 °C	313	A
	– T _j = 175 °C	$T_c = 80 \degree C$	239	A
I _{Cnom}			200	A
	_		600	A
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1200 V$	T _j = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	iode			
V _{RRM}	T _j = 25 °C		1200	V
I _F	T _j = 175 °C	T _c = 25 °C	224	Α
		T _c = 80 °C	167	Α
I _{FRM}			400	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		990	Α
Tj			-40 175	°C
Module		·		
I _{t(RMS)}			300	А
T _{stg}	module without TIM		-40 125	°C
Visol	AC sinus 50Hz, t = 1 min		4000	V

Symbol	Conditions		min.	typ.	max.	Unit
IGBT				71		_
V _{CE(sat)}	I _C = 200 A	T _j = 25 °C		1.80	2.05	V
V _{GE} = 15 V chiplevel	T _j = 150 °C		2.05	2.30	V	
V _{CE0}	chiplevel	T _j = 25 °C		0.87	1.01	V
		T _j = 150 °C		0.77	0.90	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		4.7	5.2	mΩ
		T _j = 150 °C		6.4	7.0	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}$, $I_C = 7.4$ mA		5.3	5.8	6.3	V
I _{CES}	$V_{GE} = 0 V, V_{CE} = 1200 V, T_j = 25 °C$				2.6	mA
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		12.5		nF
Coes		f = 1 MHz		-		nF
C _{res}		f = 1 MHz		0.68		nF
Q _G	V _{GE} = - 15 V+ 15 V			2087		nC
R _{Gint}	T _i = 25 °C			3.5		Ω
t _{d(on)}	$V_{GE} = +15/-15 V$ $R_{Gon} = 1 \Omega$	T _j = 150 °C		145		ns
t _r		T _j = 150 °C		43		ns
Eon		T _j = 150 °C		14		mJ
t _{d(off)}		T _j = 150 °C		457		ns
t _f		T _j = 150 °C		82		ns
E _{off}		T _j = 150 °C		22.8		mJ
R _{th(j-c)}	per IGBT				0.15	K/W
R _{th(c-s)}	per IGBT (λgrease=0.81 W/mK, thickness 50-100μm)			0.055		K/W
R _{th(c-s)}	per IGBT (λ=3.4 W		-		K/W	



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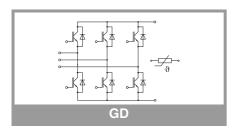
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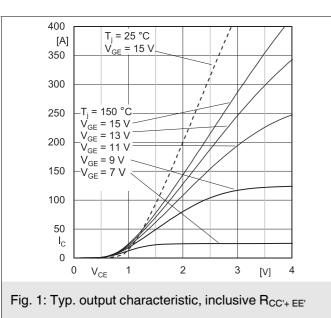
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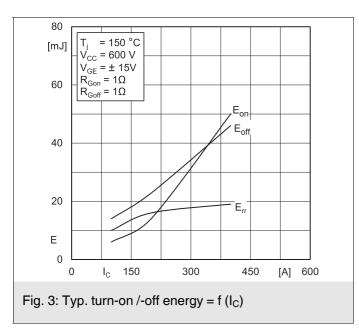
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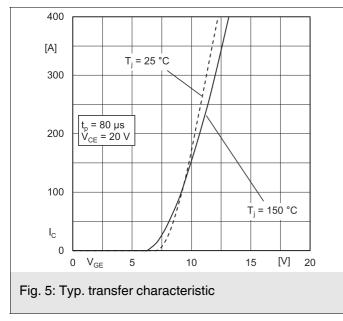
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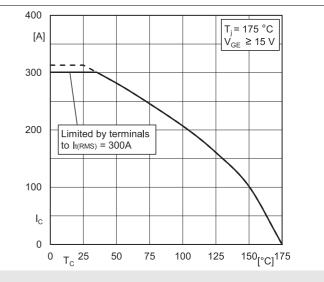
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					
•F = •EC	I _F = 200 A V _{GE} = 0 V chiplevel	T _j = 25 °C		2.20	2.52	V
		T _j = 150 °C		2.15	2.47	V
V _{F0}	- chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chiplevel	T _j = 25 °C		4.5	5.1	mΩ
		T _j = 150 °C		6.3	6.9	mΩ
I _{RRM}	I _F = 200 A	T _j = 150 °C		250		Α
Q _{rr}	di/dt _{off} = 4500 A/ μ s - V _{GE} = -15 V V _{CC} = 600 V	T _j = 150 °C		37		μC
E _{rr}		T _j = 150 °C		16		mJ
R _{th(j-c)}	per diode				0.27	K/W
R _{th(c-s)}	per diode (λgrease thickness 50-100μr		0.065		K/W	
R _{th(c-s)}	per diode (λ=3.4 W	//mK)		-		K/W
Module						
L _{CE}				20		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		1.2		mΩ
		T _C = 125 °C		1.65		mΩ
R _{th(c-s)1}	calculated without t		0.005		K/W	
R _{th(c-s)2}	including thermal c T _s underneath mod (m*K))		0.0081		K/W	
R _{th(c-s)2}	including thermal co T _s underneath mod phase change mate		-		K/W	
Ms	to heat sink (M5)		3		6	Nm
Mt		to terminals (M6)	3		6	Nm
						Nm
w				398		g
Temperat	ture Sensor					
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)			493 ± 5%		Ω
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/125} (1/T-1/T ₁₀₀)]; T[K];			3550 ±2%		к

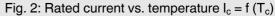


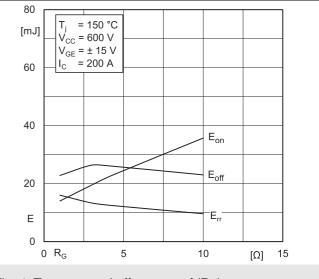


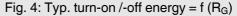












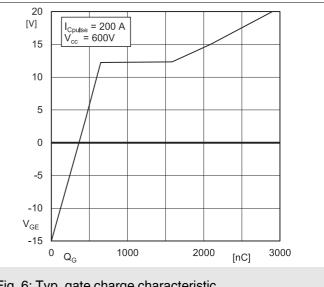
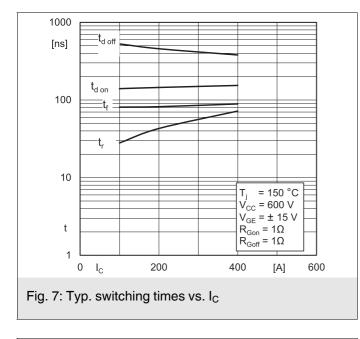
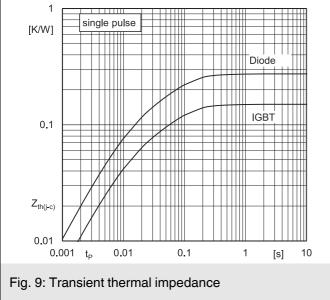
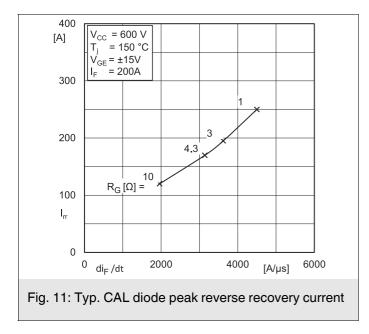


Fig. 6: Typ. gate charge characteristic







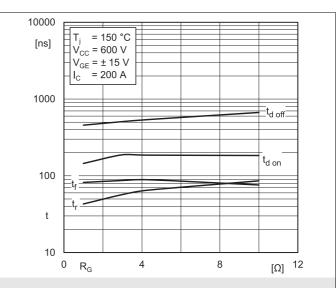
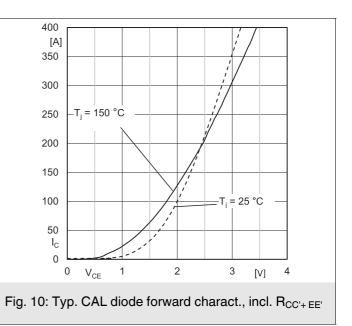
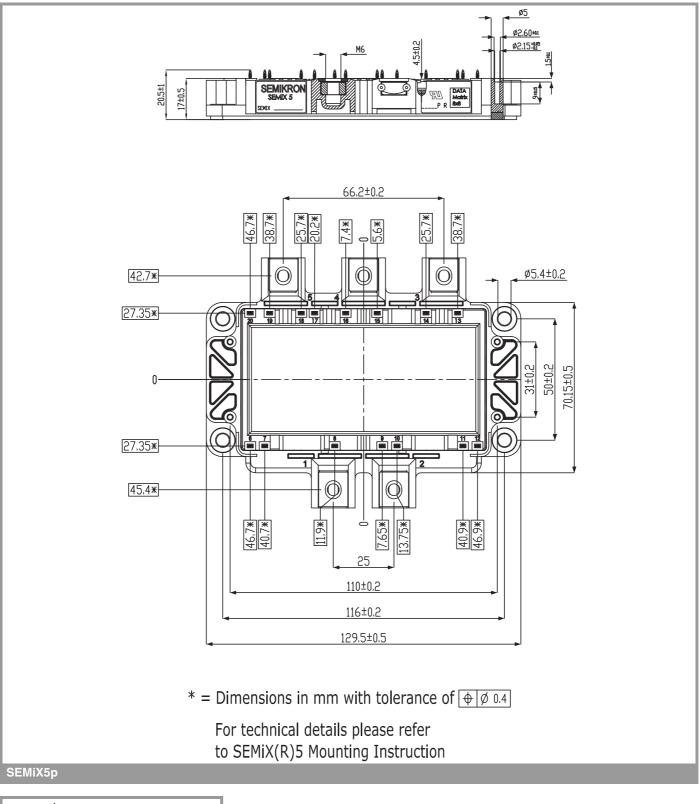
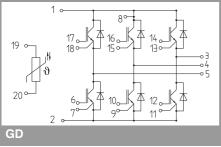


Fig. 8: Typ. switching times vs. gate resistor R_G







This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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